



		Subclass
	Class	ISSUE CLASSIFICATION

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**PATENT NUMBER**

**U.S. UTILITY Patent Application**

JCS&Y

**O.I.P.E.**

**PATENT DATE**

SCANNED

APPLICATION NO. 09/683872	CONT/PRIOR	CLASS 257	SUBCLASS 459	ART UNIT 2811 2815	EXAMINER Edie Lee
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## APPLICANTS

Joseph Iadanza

## title

Method of connecting core I/O pins to backside chip I/O pads

PTO-2040  
12/99

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
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				Amount Due	Date Paid
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